

Our Docket No: 42P14037D3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|------------------------------------|---|---------------------------|
| In re Application of: |) | |
| |) | |
| Kanapathipillai, et al. |) | Examiner: Kim, Kenneth S. |
| |) | |
| Application No: 10/649,090 |) | Art Unit: 2111 |
| |) | |
| Filed: August 27, 2003 |) | |
| |) | |
| For: Memory with Memory |) | |
| Clusters for Power Reduction in an |) | |
| Integrated Circuit |) | |
| |) | |

OFFICE ACTION RESPONSE

Mail Stop No Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir or Madam:

In response to the Office Action mailed May 1, 2007, the Applicant respectfully requests the Examiner to consider the following remarks.

THIS COMMUNICATION IS BEING TRANSMITTED

ELECTRONICALLY VIA EFS 08/01/2007